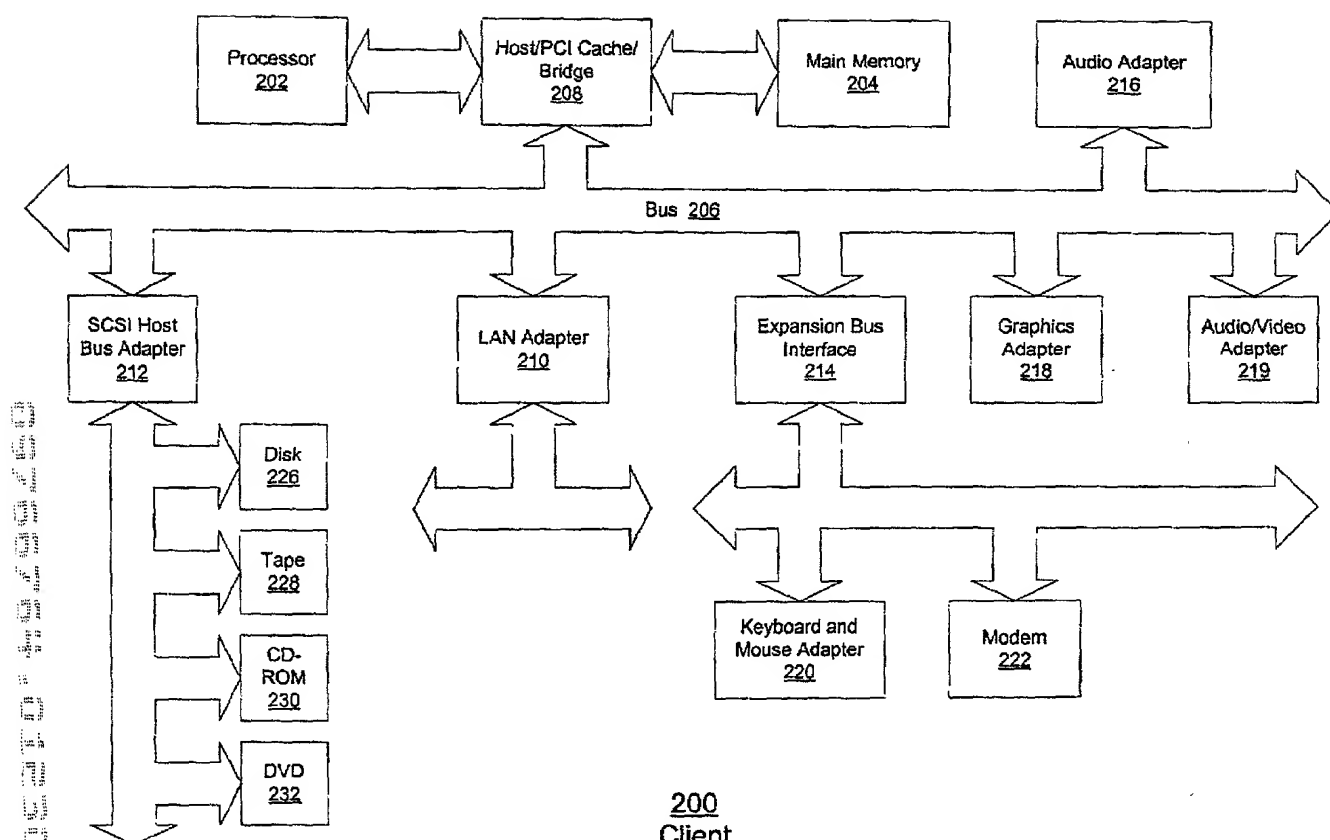
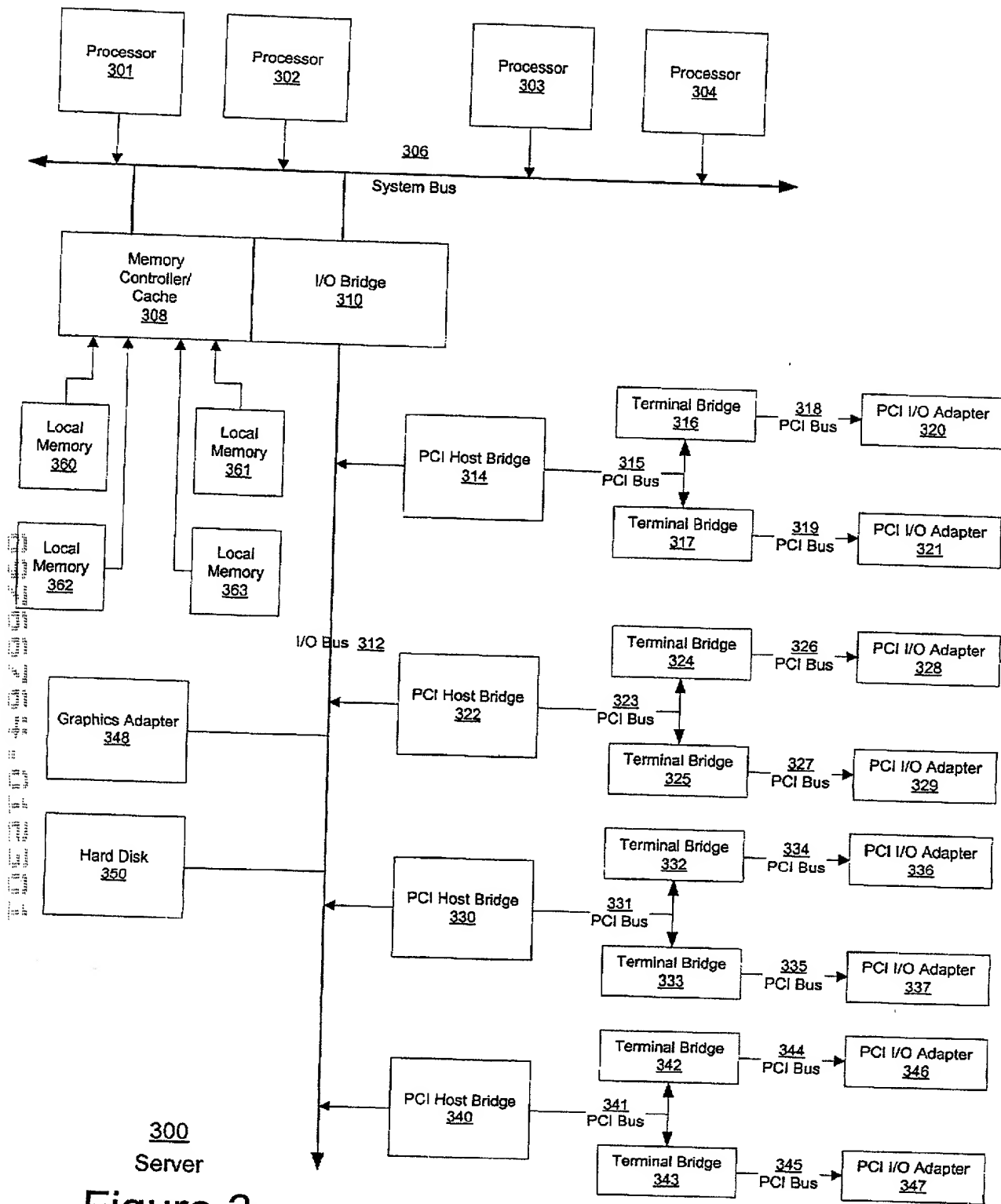


100  
Network  
Figure 1



200  
Client  
**Figure 2**



300  
Server  
**Figure 3**

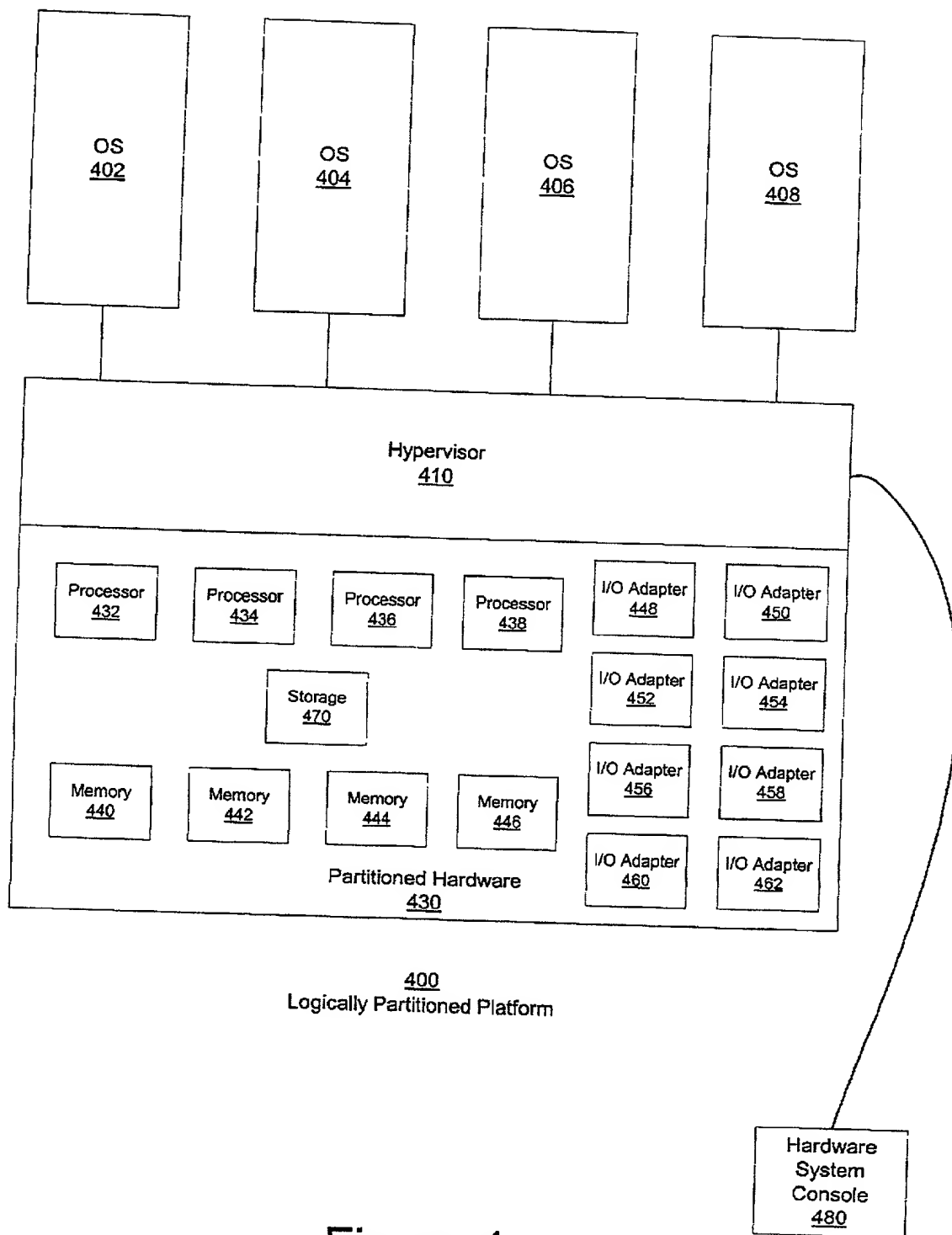


Figure 4

<u>IOA 1</u>	I/O bus DMA address 1-4
<u>IOA 2</u>	I/O bus DMA address 5-8
<u>IOA 3</u>	I/O bus DMA address 9-12

500  
I/O Bus DMA Address Range Table

**Figure 5A**

AUS990941US1

<u>OS 1</u>	IOA 1 IOA 3 Mem 1-20
<u>OS 2</u>	IOA 2 Mem 21-40

520  
Allocation Table

**Figure 5B**

AUS990941US1

Mem 5-8	I/O bus DMA addresses 1-4
Mem 11-13	I/O bus DMA addresses 9-11
Mem 25-26	I/O bus DMA addresses 5-6

550  
TCE Table

**Figure 5C**

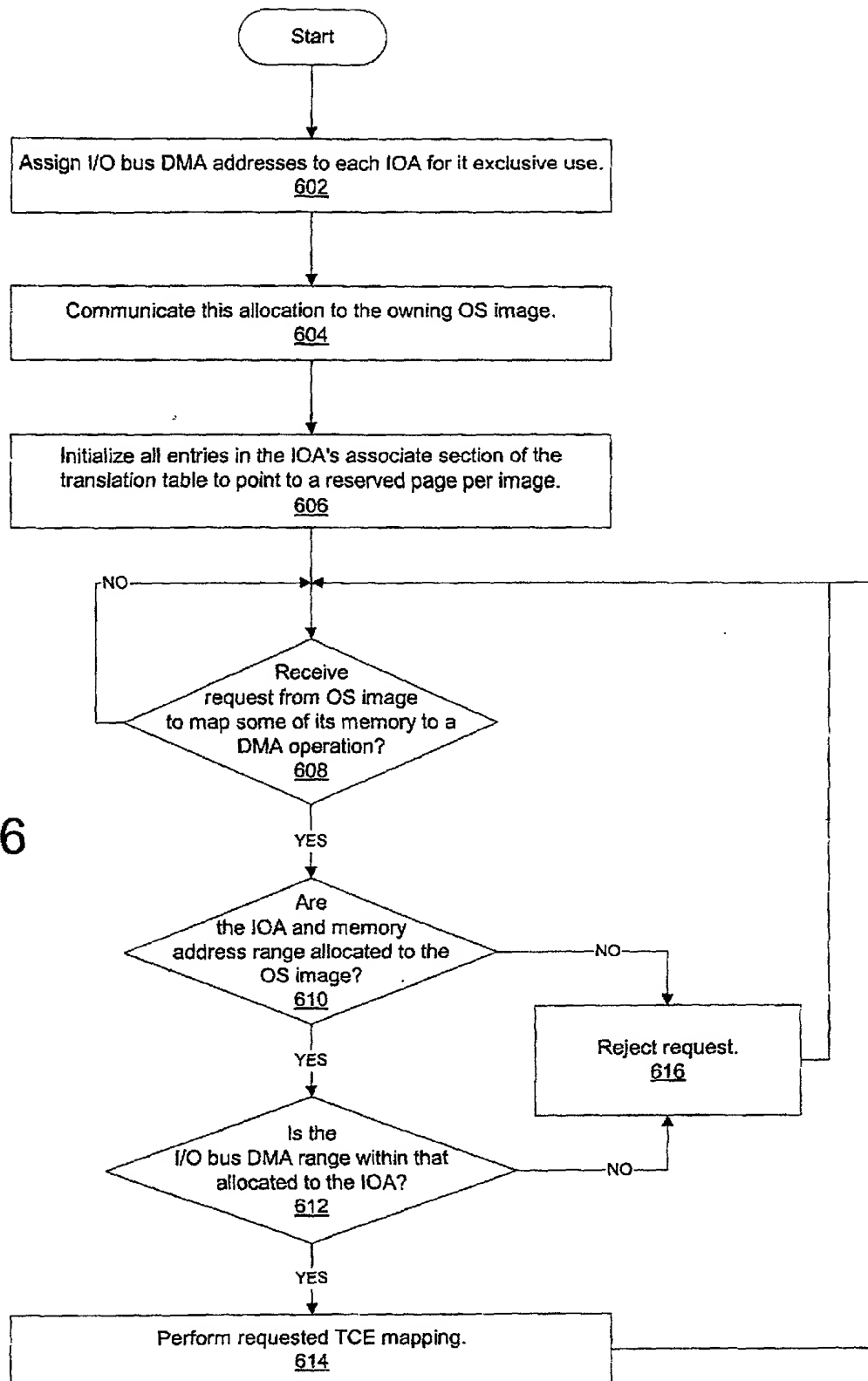


Figure 6

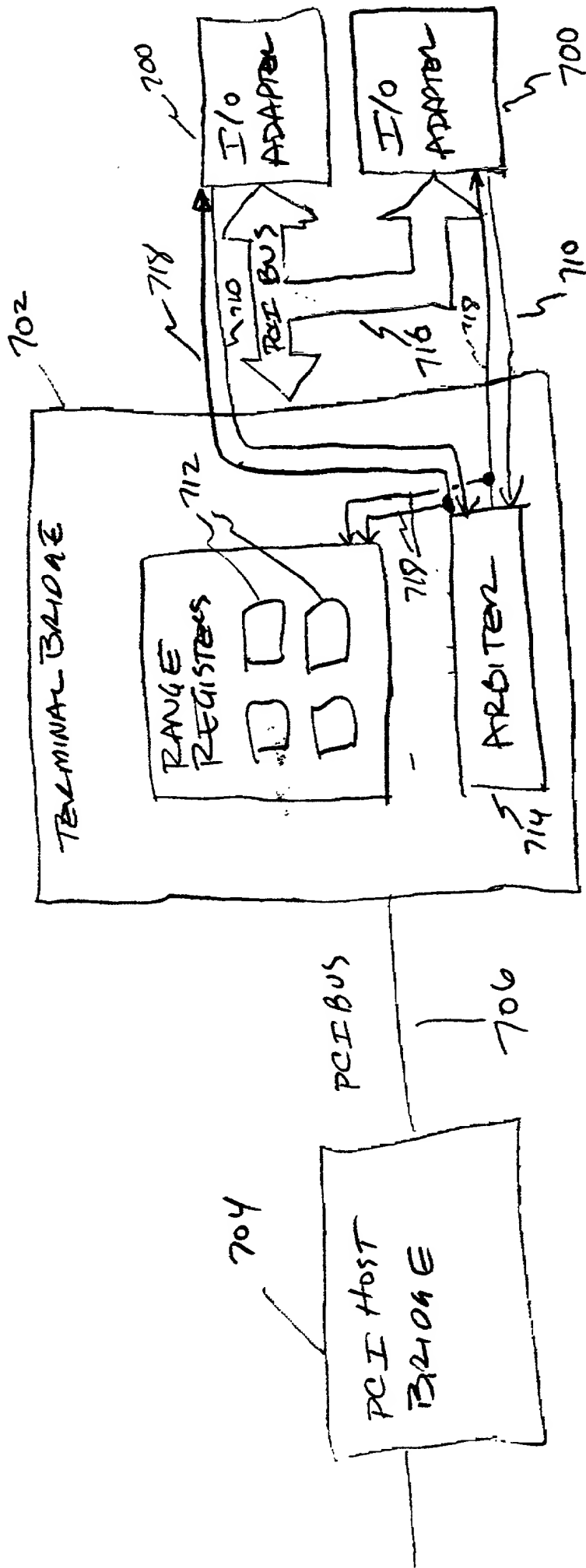


FIGURE 7